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	APPLICANT	Shaw (et al)		
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## REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	TRANSLATION	
							YES	NO
	CB	EP 0 113 516	July 18, 1984	INMOS Limited	G06F	9/46		

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	DC	<u>High Speed CMOS 4-bit Microcomputer SM-550 Series</u> , FUKUI et al., SSD82-44, December 1982, Japanese document (pp 107-109) (English translation included).
	DJ	<u>The TMS34010: An Embedded Microprocessor</u> , Karl M. GUTTAG, et al., <i>IEEE Micro</i> , vol. 8, no. 3, June 1988, pp. 39-52.
	DM	<u>The IBM System/ 360 Model 91: Machine Philosophy and Instruction-Handling.</u> , D.W. Anderson et al., <i>IBM Journal</i> , January 1967, pp. 8-24.
	DN	<u>GE 625/635 Programming Reference Manual</u> , <i>General Electric</i> , July 1964, Rev. January 1966.
	DP	<u>IBM RT PC Romp Processor And Memory Management Unit Architecture</u> , Simpson & Hester, <i>IBM Systems Journal</i> , Vol. 26, No. 4, 1987, pp. 346-360.
	DQ	<u>i860 64-bit Microprocessor: Advance Information</u> , February 1989.
	DT	<u>MIPS: A Microprocessor Architecture</u> , John Hennessy et al., <i>IEEE</i> , 1982, pp. 17-22.
EXAMINER		DATE CONSIDERED